## **Supplementary Information Section 5**

## **S5.** Noise Characterization

Noise is characterized by recording a time series of ionic current at a constant voltage below  $\pm 1V$ . We examined noise in the ionic current flowing through our fabricated nanopores, by performing power spectral density (PSD) measurements to determine their noise level. These measurements were performed using Axopatch 200B (Molecular Devices) as the current amplifier with the 4-pole Bessel filter set at 100kHz. Figure S6 shows the PSD of different membranes containing fabricated nanopores under an applied trans-membrane potential of 200mV, in 1M KCl pH8. Remarkably, the PSD for these nanopores fabricated in solution reveals low 1/f noise level, comparable to the best TEMdrilled nanopores [1,2], and in some instances ultra-low 1/f noise level can be attained, which are comparable to biological pores[1]. We noticed that, similarly to TEM-drilled pores, nanopores created by dielectric breakdown do exhibit variability in their lowfrequency noise level, though low 1/f noise level can be obtained with very high yield (>80%). Nevertheless, in the event of a high noise nanopore, the low-frequency noise can be reduced following a conditioning procedure similar to Beamish et al.[3]. The fact that we can reliably produce nanopores with low 1/f noise may be attributed to the fact that our nanopores are created directly in aqueous solution and are never exposed to air, thus eliminating wetting issues, and minimizing the likelihood of nanobubbles trapped at the vicinity of a pore[4]. In addition, we speculate that the nanopore's inner wall surface can be chemically modified by the fabrication process, which may contribute to an improved noise performance.

## References:

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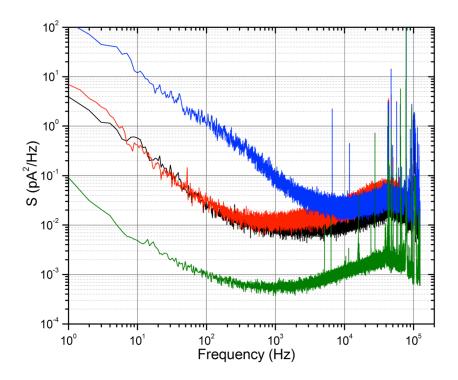


Figure S6: Power spectrum densities of the ionic current of four nanopore fabricated by controlled dielectric breakdown, recorded by an Axopatch 200B at 200mV. Data sampled at 250kHz, and low-pass filtered at 100kHz by a 4-pole Bessel filter. *Blue curve* - pore is 6-nm in diameter, created at pH2 in 1M KCl. *Red curve* - pore is 6-nm in diameter, created at pH2 in 1M KCl. *Black curve* - pore is 6-nm in diameter, created at pH2 in 1M KCl. *Green curve* - pore is 12-nm in diameter, created at pH13.5 in 1M KCl. The chip is covered with polydimethylsiloxane (PDMS) to minimize noises from membrane capacitance[1]. At this time, there does not yet appear to be any obvious relationship between noise levels and fabrication conditions.