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**RESEARCH ARTICLE** 

# High-performance fractional order terminal sliding mode control strategy for DC-DC Buck converter

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## Abstract

This paper presents an adaption of the fractional order terminal sliding mode control (AFTSMC) strategy for DC-DC Buck converter. The following strategy aims to design a novel nonlinear sliding surface function, with a double closed-loop structure of voltage and current. This strategy is a fusion of two characteristics: terminal sliding mode control (TSMC) and fractional order calculation (FOC). In addition, the influence of "the controller parameters" on the "performance of double closed-loop system" is investigated. It is observed that the value of terminal power has to be chosen to make a compromise between start-up and transient response of the converter. Therefore the AFTSMC strategy chooses the value of the terminal power adaptively, and this strategy can lead to the appropriate number of fractional order as well. Furthermore, through the fractional order analysis, the system can reach the sliding mode surface in a finite time. And the theoretical considerations are verified by numerical simulation. The performance of the AFTSMC and TSMC strategies is tested by computer simulations. And the comparison simulation results show that the AFTSMC exhibits a considerable improvement in terms of a faster output voltage response during load changes. Moreover, AFTSMC obtains a faster dynamical response, smaller steady-state error rate and lower overshoot.

### Introduction

DC–DC power converters are widely applied for supplying various output voltage in many electric vehicular systems, such as DC motor drives, the hybrid energy storage system (HESS), battery equalization and so on [1]. Our research team is mainly engaged in the research of HESS. The HESS contains two or more power sources connected by DC-DC converters. In order to improve the efficiency and performance of the HESS, the high-performance control strategy for DC-DC converters is needed. But DC-DC converters are inherently non-linear system with chaotic circuit. So the stability of the DC-DC converters is very important [2]. Therefore, the design of high-performance control strategy is usually a challenging issue.

The DC-DC converters in HESS are almost always multiple topologies including several MOSFET switches, so they are workable in both Buck mode and Boost mode. The power



Fig 1. DC-DC Buck converter topology.

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consumption of DC-DC converter is an important consideration for the HESS system, when compared with the power consumption of controller [3]. The power consumption of electronic components is a major part of the total power consumption in HESS. Therefore, we created the DC-DC converter structure with simplified design to reduce the power consumption. The control performance of DC-DC converter is studied in this paper. The control strategies of Buck and Boost converters have some similarities, so in this paper we just choose the Buck converter to investigate our novel control strategies.

The sliding mode control (SMC) has many advantages, such as its fast dynamic response, robustness to disturbances, guaranteed stability and simplicity in implementation [4]. There have been a lot of researches on sliding mode control for DC-DC converters. In Ref. [5], Hasan Komurcugil proposed an adaptive terminal sliding mode control strategy for Buck converter, and his sliding surface is a linear one based on linear combination of the system states, using an appropriate time-invariant coefficient. In Ref. [6], Yanmin Wang and her partner designed a double closed-loop structure for DC-DC converter feedback control, and the double closed-loop have a smaller steady-state error than others. In Ref. [7], Junxiao Wang and his partner investigated the performance of the nonlinear disturbance observers with the sliding mode control for Buck converter. In Ref. [8–11], the authors pointed out a fractional order calculation applied in DC-DC converter and control respectively.

In this paper, we focus on the high-performance control strategy for the DC-DC Buck converter, and propose a novel method of fractional order on terminal sliding mode control (FTSMC). Then utilize the method to design a novel nonlinear sliding surface function (based on the double closed-loop structure) that is a fusion of characteristics of TSMC and FOC [12–13].

The rest of the paper is organized as follows: Section 2 introduces the basic principles of the DC-DC Buck converter. Section 3 deals with the design of terminal sliding surfaces for DC-DC Buck converter. Section 4 conducts the design of nonlinear controllers for the Buck converter based on the fractional order calculation and the terminal sliding mode control. Section 5 shows simulation results and the adaptive methods to determine the terminal power parameter values. Section 6 states some conclusions and guidelines for further works.

### Modeling the DC-DC Buck converter

The topology of DC-DC Buck converter is shown in  $\underline{Fig 1}$ , and it consists of a DC input voltage source, a MOSFET switch, a diode, an inductor, a capacitor and a load resistor. The average state equations describing the operation of the Buck converter can be written as

$$\frac{di_L}{dt} = \frac{1}{L} \left( uV_{in} - V_o \right) \tag{1}$$

$$\frac{dV_o}{dt} = \frac{1}{C} \left( i_L - \frac{V_o}{R} \right) \tag{2}$$

where u is the control input that takes 1 for the ON state of the switch, and 0 for the OFF state [14].

Let us define the output voltage error,  $x_1$  is

$$x_1 = V_o - V_{ref} \tag{3}$$

Where  $V_{ref}$  is the reference value of the output voltage. By taking the time derivative of (3),  $x_2$  which is the rate of change of voltage error can be expressed as

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$$x_2 = \dot{x}_1 = \dot{V}_o - \dot{V}_{ref} \approx \dot{V}_o \tag{4}$$

The state-space model of the Buck converter can be transformed to

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$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 0 & 1 \\ -\frac{1}{LC} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} 0 \\ uV_{in} - V_{ref} \\ LC \end{bmatrix}$$
(5)

### Terminal sliding mode control for the Buck converter

Most of the TSMC strategies are commonly used for the linear sliding surface, which is based on a linear combination of the system states, using an appropriate and time-invariant coefficient [15]. Therefore, the value of coefficient has to be chosen and this is an embarrassment [16–17]. Considering the confusion of this embarrassment, we designed a terminal sliding surface function with a double closed-loop structure of voltage and current. The function *S* can be defined as

$$S = i_{L}(t) - k_{a}x_{1}^{\gamma} - k_{b}\int_{0}^{t} x_{1}^{\gamma}d\tau$$
(6)

Where  $k_a > 0$ ,  $k_b > 0$ ,  $0 < \gamma < 1$ , and they are positive odd integers. When the system is in the terminal sliding mode, it means the Eq (6) is equal to 0 (S = 0), assuming

$$y = \int_0^t x_1^{\gamma} d\tau \tag{7}$$

The system dynamics can be determined by the following nonlinear differential equation

$$k_a \dot{y} = i_L(t) - k_b y \tag{8}$$

Note that Eq (8) can also be written as

$$dt = \frac{k_a}{i_L - k_b y} dy \tag{9}$$

Taking integral of both side of Eq (9) and evaluating the resulting equation on the closed interval  $(x_1(0) \neq 0, x_1(t_s) = 0)$ , the finite time  $t_s$  is obtained by

$$t_s = \left| -\frac{k_a}{k_b} \ln(i_L - \frac{k_b}{1+\gamma} x_1(0)^{1+\gamma}) \right|$$
(10)

From Eq (10), it is obvious that the convergence time  $t_s$  still depends on the parameters  $k_a$ ,  $k_b$  and  $\gamma$ . Therefore, these parameters must be carefully selected to ensure the desired response.

The sufficient condition for the existence of the terminal sliding mode is given by

$$S\dot{S} < 0 \tag{11}$$

Select the Lyapunov function as

$$V = \frac{1}{2}S^2 \tag{12}$$

The time derivative of Eq (12) can be written as

$$\dot{V} = S\dot{S} \tag{13}$$

In order to satisfy the Lyapunov function, the deviation from the terminal sliding surface *S* and its time derivative,  $\dot{S}$  should be opposite signs in the vicinity of a sliding surface.

When u = 1, S < 0, so need  $\dot{S} > 0$ 

$$\dot{S} = \frac{1}{L} (V_{in} - V_o) - k_a \cdot \gamma \cdot x_1^{\gamma - 1} \cdot \frac{1}{C} (i_L - \frac{V_o}{R}) - k_b x_1^{\gamma} > 0$$
(14)

When u = 0, S > 0, so need  $\dot{S} < 0$ 

$$\dot{S} = -\frac{1}{L}V_o - k_a \cdot \gamma \cdot x_1^{\gamma - 1} \cdot \frac{1}{C}(i_L - \frac{V_o}{R}) - k_b x_1^{\gamma} < 0$$
(15)

Make the value of  $\gamma$ , *i*<sub>L</sub> approximately equal to 1 and 0 respectively, the conditions that limit the existence region of the design parameters are obtained as

$$0 < k_a < \frac{CR}{L} \tag{16}$$

$$0 < k_b \le \frac{k_a}{CR} \tag{17}$$

From Eq (6) and  $\dot{S} = 0$ , the equivalent control law  $u_{eq}$  in this case becomes

$$u_{eq} = \frac{L}{V_{in}} \left[ \frac{k_a \cdot \gamma \cdot x_1^{\gamma - 1}}{C} \left( i_L - \frac{V_o}{R} \right) + k_b \cdot x_1^{\gamma} + \frac{V_o}{L} \right]$$
(18)

From expression Eq (18) using the constraint  $|u_{eq}| \le 1$ , and considering the aforementioned equilibria conditions, the conditions that limit the existence region of the design parameters are obtained as

$$0 < k_a < \frac{CR}{L} \frac{V_{in} - V_{ref}}{V_{ref}}$$
<sup>(19)</sup>

$$0 < k_b \le \frac{V_{in}}{LV_{ref}} \tag{20}$$

To solve the inequality Eqs (16), (17), (19) and (20), we should determine the parameters value approximately.

# Fractional order terminal sliding mode control for the Buck converter

The terminal sliding surface function is expressed as a fractional order differential equation that is obtained in the form

$$S = i_L(t) - k_a x_1^{\ \gamma} - k_b D_0^{-\lambda} x_1^{\ \gamma} \tag{21}$$

Where  $\gamma \in [0,1]$ ,  $\lambda \in [0,1]$ ,  $k_a k_b$  are positive constant. For the Buck converter with FTSMC, the time derivative of Eq (21) can be written as

$$\dot{S} = \dot{i}_L(t) - k_a \cdot \gamma \cdot x_1^{\gamma - 1} \cdot \dot{x}_1 - k_b D_0^{1 - \lambda} x_1^{\gamma}$$
(22)

Following the procedure of the previous section, the obtained expression for equivalent control is:

$$u_{eq} = \frac{L}{V_{in}} \left[ \frac{k_a \cdot \gamma \cdot x_1^{\gamma - 1}}{C} (i_L - \frac{V_o}{R}) + k_b D_0^{1 - \lambda} x_1^{\gamma} + \frac{V_o}{L} \right]$$
(23)

To obtain the sliding mode dynamics, we insert (21) into (5), and find that the whole closed loop system is in fractional order. Obviously, it is more appropriate to analyze the stability and convergence via the fractional version of Lyapunov by direct method [18-21].

Selecting the Lyapunov function as

$$V = S^2 \tag{24}$$

It follows from the Ref. [22], if 0 is the equilibrium point of system (21) and  $x(0) = x_0$ , the fractional order derivative of Eq (24), can be written as

$$D^{1-\lambda}V = D^{-\lambda}\dot{V} \leq -KD^{-\lambda} ||x_1||$$
  
=  $-Kl^{-1}D^{-\lambda}||S|| \leq -Kl^{-1}||D^{-\lambda}S||$   
=  $-Kl^{-1}||x_1||$  (25)

Where K is positive constant, *l* is Lipschitz constant and l > 0. So, we can find V > 0 and  $D^{1-\lambda}V < 0$ . In other words, the controlled system satisfies the reaching condition.

When the system reaches the sliding surface, which is S = 0, it is in the "terminal sliding" mode. Its dynamics can be determined by the following equation:

$$k_a x_1^{\ \gamma} = i_L(t) - k_b D_0^{-\lambda} x_1^{\ \gamma} \tag{26}$$

We know, several reputed definitions for fractional derivatives are put forward, including Riemann-Liouville definition, Grunwald-Letnikov definition, Caputo definition, Weyl definition, and Marchaud definition [23]. Among them, Riemann-Liouville definition has been well studied. So, we use Riemann-Liouville definition for fractional order differential operation as

$$k_b D_0^{1+\lambda} (D_0^{-1-\lambda} \dot{x}_1^{\gamma}) = D_0^{1+\lambda} (i_L(t) - k_a x_1^{\gamma})$$
(27)

Taking fractional integral of both side of Eq (27), the finite time  $t_s$  is obtained by

$$t_{s} = \left| -\frac{k_{b}\Gamma(\gamma+\lambda)}{k_{a}\Gamma(\gamma+1)} \ln(i_{L} - \frac{k_{b}x_{1}(0)^{2+\gamma-\lambda}}{(2+\gamma-\lambda)}) \right|$$
(28)

Therefore, it can be concluded that system trajectories can reach the equilibrium point in a finite time. When  $\lambda = 1$ , it is obvious that (28) is equivalent to (10). It means that the finite time taken to attain the equilibrium point of the FTSMC system, is the same as the one of the TSMC system, as given in (10).

### Adaptive strategy and simulation results

In order to show the performance of the FTSMC, the DC-DC Buck converter system was subsequently tested by simulations. Simulations are carried out using MATLAB/Simulink. The Simulation framework is shown in <u>S1 Fig</u>, and parameters of Buck converter are given in <u>Table 1</u>.

Descriptions	Parameters	Nominal values
Input voltage	V <sub>in</sub>	25V
Desired output voltage	V <sub>ref</sub>	10V
Inductance	L	260 uH
Capacitance	С	100 uF
Load resistance	R	1~10Ω

#### Table 1. Specifications of Buck converter.

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From Table 1 and the Eqs (16), (17), (19) and (20), we chose  $k_a = 0.8 k_b = 780$ , and the value range of terminal power ( $\gamma$ ) is between 0 and 1, the performance of the proposed integer-order terminal sliding mode control strategy are showed in Fig 2.

It is clear from Fig 2(A) that the output voltage responses at the start-up become faster with increasing the value of  $\gamma$ . But the large value of  $\gamma$  can make overshoots and take a long time to reach the equilibrium point of the Eq (10). Fig 2(B) shows the responses of the output voltage for step changes in R (from 10  $\Omega$  to 1 $\Omega$ ), which are obtained by the SMC method with  $\gamma = 1$ , and the TSMC method with different  $\gamma$  values. Unlike the start-up case, it is interesting to note that the output voltage responses become faster with decreasing the value of  $\gamma$ . Therefore, the value of  $\gamma$  is chosen as some constant, to make a compromise between start up and transient responses of the converter.

When  $x_1$  is near the equilibrium point, it can be seen as  $|x_1| < 1$ , the  $\gamma$  leads to  $|x_1^{\gamma}| > |x_1|$ . In such a case, the system state with the nonlinear term  $x_1^{\gamma}$  converges toward equilibrium point faster than the linear term  $x_1$ . On the other hand, when  $|x_1| > 1$ , the  $\gamma$  leads to  $|x_1^{\gamma}| < |x_1|$ , it means the system state with the nonlinear term  $x_1^{\gamma}$  converges toward equilibrium point slower than the linear term  $x_1$ .





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Fig 3. The output voltage dynamic response with adaptive  $\gamma$ . (a) The output voltage dynamic response in start-up; (b) The output voltage dynamic response during load variations.

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So, we proposed the adaptive law to choose the value of  $\gamma$ , which builds a monotone increasing function  $x_1$  for  $\gamma$ . This function will choose the value of  $\gamma$  approximately equal to 1 when  $|x_1| > 1$ , and choose the value of  $\gamma$  much smaller but not less than 0.25 when  $|x_1| < 1$ . According to the boundary conditions and the Simulink results, we use MATLAB/CFTOOL to fit the function of  $x_1$  for  $\gamma$ , describe it as

$$\gamma = \frac{1}{\pi} \arctan(x_1 - 0.99) + 0.5 \tag{29}$$



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Descriptions	k <sub>a</sub>	κ <sub>b</sub>	γ	λ
SMC	0.8	780	1	1
ATSMC	0.8	780	adaptive	1
AFTSMC	0.8	780	adaptive	0.7

Table 2. Controller parameters of the proposed m	methods.
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Fig 5. The output voltage dynamic response with different strategy.

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From Eq (29), according to the state error,  $\gamma$  is selected adaptively, the simulation result as shown in Fig 3. We can easily observe the dynamic performance of the adaptive strategy better than the other constant terminal power.

Further, we investigate the dynamic response of output voltage with different fractional order ( $\lambda$ ) on the basis of the adaptive terminal sliding mode control strategy. Fig 4 shows the simulated start-up and transient responses of the output voltage obtained by AFTSMC strategies with different  $\lambda$  values. It is interesting to note that the output voltage responses become faster with decreasing the value of  $\lambda$ , but when  $\lambda = 0.5$ , the overshoot of the system appears and exceeds 25%. In order to obtain high performance control strategy, we should try to avoid the voltage overshoot and chattering. So choosing  $\lambda = 0.7$  is our choice for ideal parameter value.

In order to compare the control effect of TSMS, ATSMC, and AFTSMC, we use the parameter selection as shown in <u>Table 2</u>. As shown in <u>Fig 5</u>, the response time of the system with AFTSMC is less than others. At t = 0.05 s, the load resistance is changed from  $10\Omega$  to  $1 \Omega$ . Therefore, the output current will be increased, and the output voltage has a short step-down. It can be seen that the output voltage returns faster to reference output voltage in AFTSMC.

### Conclusions

The fractional order terminal sliding mode control (FTSMC) based on a double closed-loop structure of voltage and current has been proposed. The influence of the controller parameters was investigated. It is observed that the chosen value of terminal power aims to make a compromise between the start-up and the moment when load changes. For this matter, we proposed an adaptive law to choose the terminal power, and the simulation shows that the method is effective. Further, we investigated the dynamic response of output voltage with different fractional orders, on the basis of the adaptive terminal sliding mode control strategy. It is shown that when the fractional order ( $\lambda$ ) equal to 0.7, the performance of dynamic responses is better than others. In addition, the simulation results show that the AFTSMC strategy has

the better performance in comparison with the ATSMC and TSMC. The novel fractional terminal sliding mode control exhibits considerable improvement in terms of a faster output voltage response, in the start-up and during load changes.

### **Supporting information**

**S1 Fig. Simulation framework diagram.** In this simulation framework, the sliding surface function *S* could be the integer or fractional order terminal sliding surface function. (TIF)

**S2 Fig. The output voltage dynamic response with different strategy.** The control strategies include terminal sliding mode control (TSMC), adaptive terminal sliding mode control (ATSMC), and adaptive fractional order terminal sliding mode control (AFTSMC) respectively.

(TIF)

### **Author Contributions**

Conceptualization: Huan Zhou.

Methodology: Jianlin Wang, Dan Xu.

Software: Huan Zhou.

Supervision: Anning Bai.

Writing - review & editing: Wei Lu.

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